

# 1/f Noise Modeling at Low Temperature with the EKV3 Compact Model

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**NSTI Nanotech Conference, May 3-7, 2009 (Houston, TX)**

# Outline

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- **Introduction**
- **CMOS process, characterization & specific effects at low temperature (LT)**
- **Low frequency (LF) noise measurements at room & low temperature and physical models**
- **Compact modeling of LF noise with EKV3**
- **Conclusion**

## Introduction: aim of this work

- Design of hybrid CMOS read-out circuits for night vision with high performance (cooled) IR imagers
- CMOS circuits also cooled at LT (77 - 200 K)
- Modeling difficulties: low temperature & analog circuits (weak/moderate inversion)
- IC design with 0.8, 0.5 and 0.35  $\mu\text{m}$  CMOS and circuit simulation with the EKV2.6 charge based compact model
- More and more transistors in the pixel, need for more and more advanced CMOS processes
- 0.18  $\mu\text{m}$  process: new effects, e.g. INWE (STI), DITS (pockets)
- WCM 2008 (Boston): EKV3 is an accurate compact model for LT analog circuits design
- This work: test of the EKV3 LF noise model for analog transistors

# CMOS process & characterization

## Process

- Commercial process, optimized for room temperature
- 0.18  $\mu\text{m}$  - 1.8 V ( $T_{\text{ox}}=3.3$  nm) & 0.35  $\mu\text{m}$  - 3.3 V (6.5 nm)
- Different transistors: standard (STD), low  $V_{\text{th}}$  (LVT), zero  $V_{\text{th}}$  (ZVT)

## DC characterization

- 1<sup>st</sup> step before LF noise measurements  $\Rightarrow$  std. and specific effects
- $I_d$ ,  $G_m$ ,  $G_{mb}$ ,  $G_{ds}$   $\Rightarrow$  thermal noise
- Temperature range: 77 K - 300 K

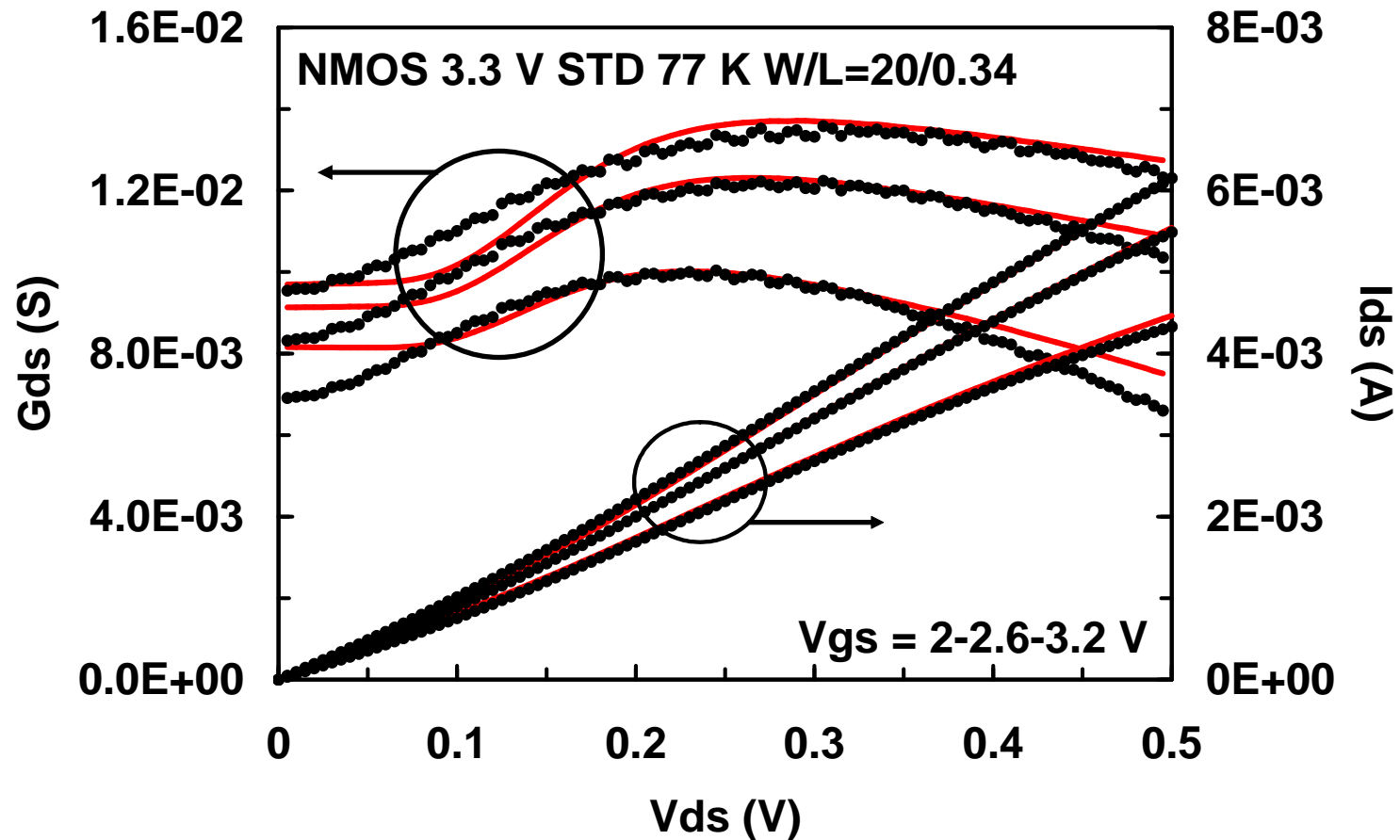
## LF noise measurements

- Transistors biased in saturation:  $V_{ds}=V_{DD}$  as in analog circuits
- $L=L_{\text{min}}$  - 20  $\mu\text{m}$
- $W=W_{\text{min}}$  - 20  $\mu\text{m}$
- $1/f$   $\gamma$  noise with  $\gamma = 1.0 \pm 0.1$

# Specific effects at LT: 1<sup>st</sup> example

⇒ Freeze-out in LDD zones + ioniz. by lateral field

..... meas.    — sim. with EKV3 (modified Verilog-A code)



# Specific effects at LT: 2<sup>nd</sup> example

Peak in Gm on lightly doped NMOSFET ( $< 8 \cdot 10^{16} \text{ cm}^{-3}$ )

⇒ Sub-bands quantization effect (Cryogenics, 2009)

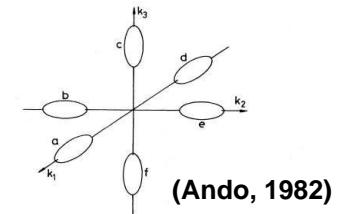
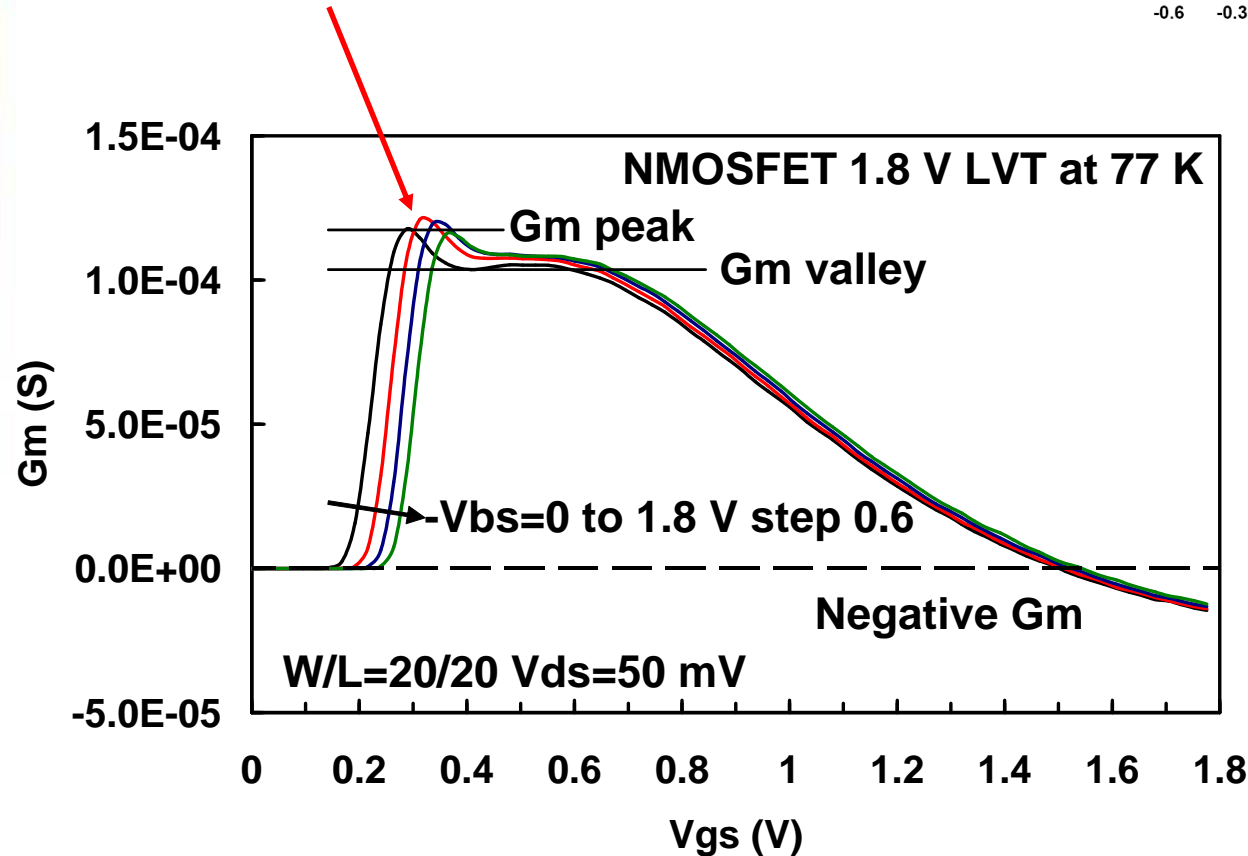
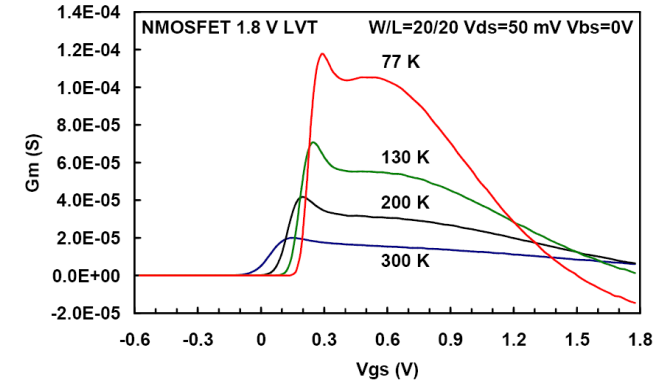


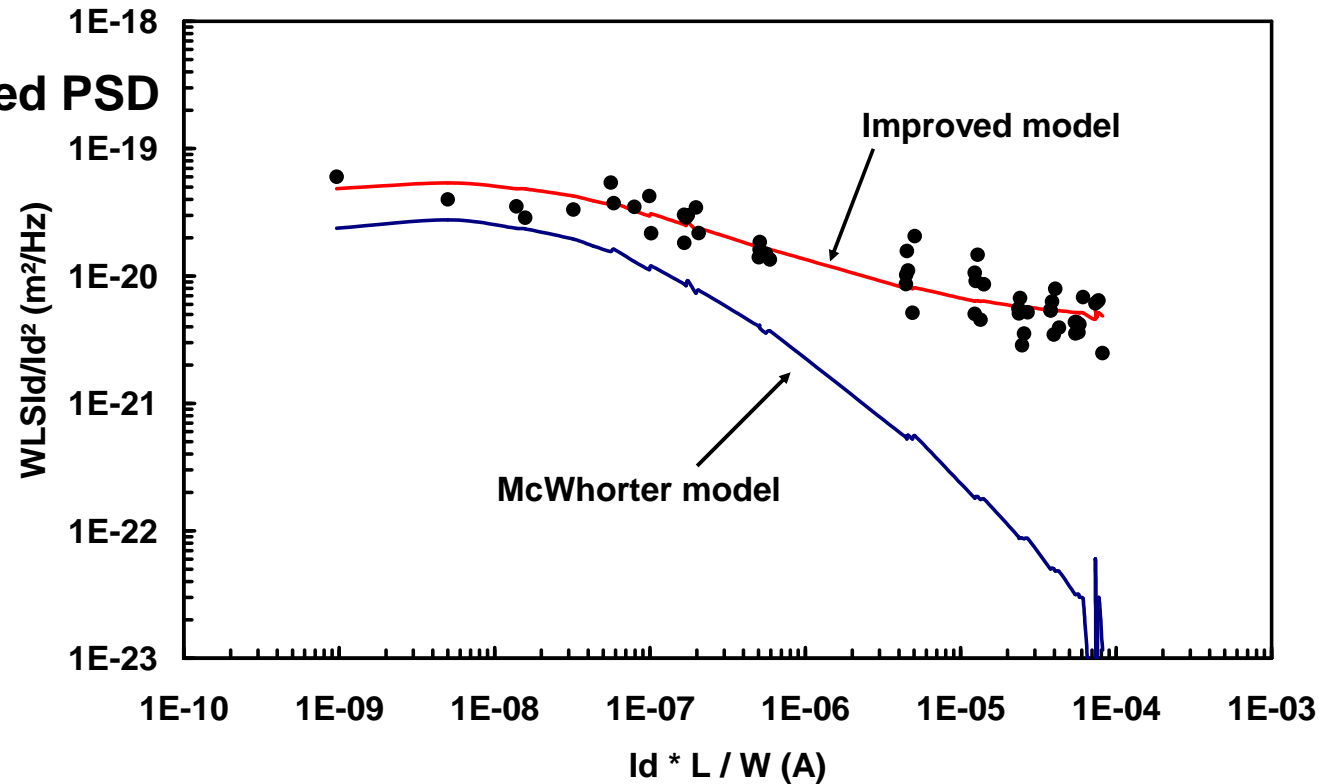
FIG. 5. Schematic constant-energy surfaces for the conduction band of silicon, showing six conduction-band valleys in the (100) direction of momentum space. The band minima, corresponding to the centers of the ellipsoids, are 35% of the way to the Brillouin-zone boundaries. The long axis of an ellipsoid corresponds to the longitudinal effective mass of electrons in silicon,  $m_l=0.916m_0$ , while the short axes correspond to the transverse effective mass,  $m_t=0.190m_0$ .

# 1/f noise measurements at room temperature

Normalized PSD



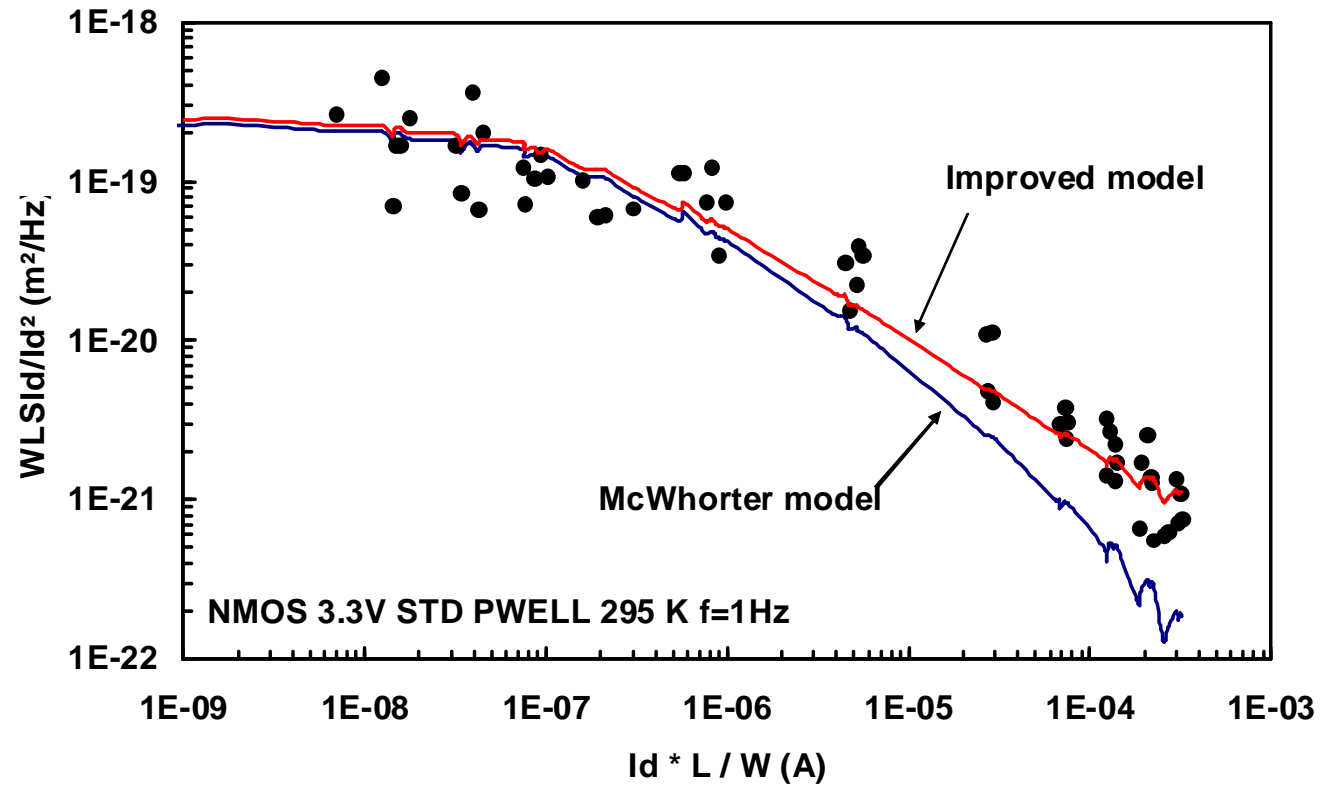
PMOSFET 3.3 V  
295 K,  $f=1$  Hz



- Plateau in weak inversion: Hooge mobility fluctuation model not applicable
- McWhorter's carrier number model applicable, but has to be improved in strong inversion

# 1/f noise measurements at room temperature

**NMOSFET 3.3 V  
295 K, f=1 Hz**



⇒ Same improved model applies for NMOSFET

# The improved McWhorter's LF noise model

## 1 - Standard McWhorter carrier number fluctuations

$$SI_d = \left[1 + \alpha \mu C_{ox} \frac{I_d}{G_m}\right]^2 G_m^2 S_{vfb}(f)$$

units: A<sup>2</sup>/Hz

## 2 - Correlated mobility fluctuations at high current

$$S_{vfb}(f) = \frac{kT q^2 \lambda N_t(E_F)}{WL C_{ox}^2} \frac{1}{f^\gamma}$$

units: V<sup>2</sup>/Hz

$S_{vfb}$ : flat-band voltage fluctuations

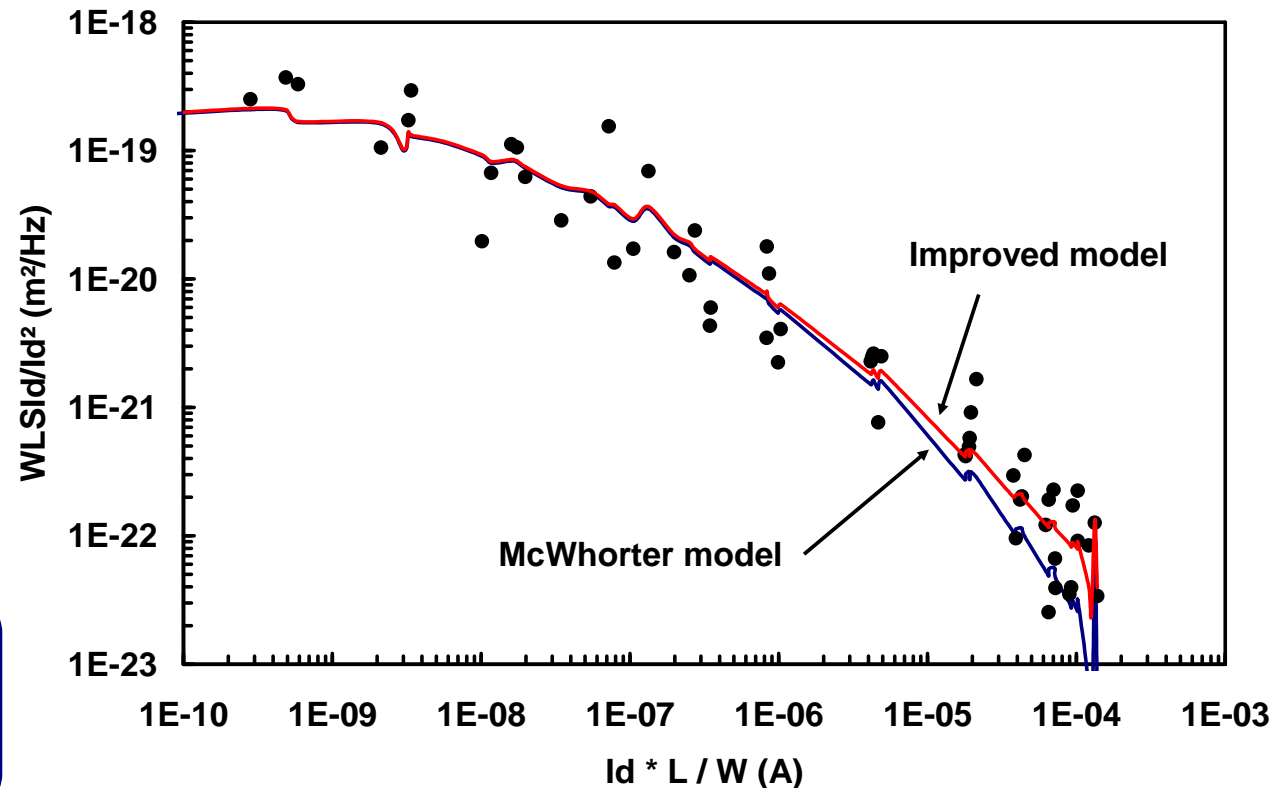
$N_t$ : oxide trap volume density per unit energy at  $E_F$  (eV<sup>-1</sup> cm<sup>-3</sup>)

$\lambda$ : tunnel attenuation distance (0.1 nm)

$\alpha$ : Coulomb scattering coefficient (Vs/C) ~ 10<sup>4</sup> for electrons, 10<sup>5</sup> for holes at RT

For 3.3 V STD p-MOSFETs:  $\alpha=3 \times 10^5$  @ 295 K

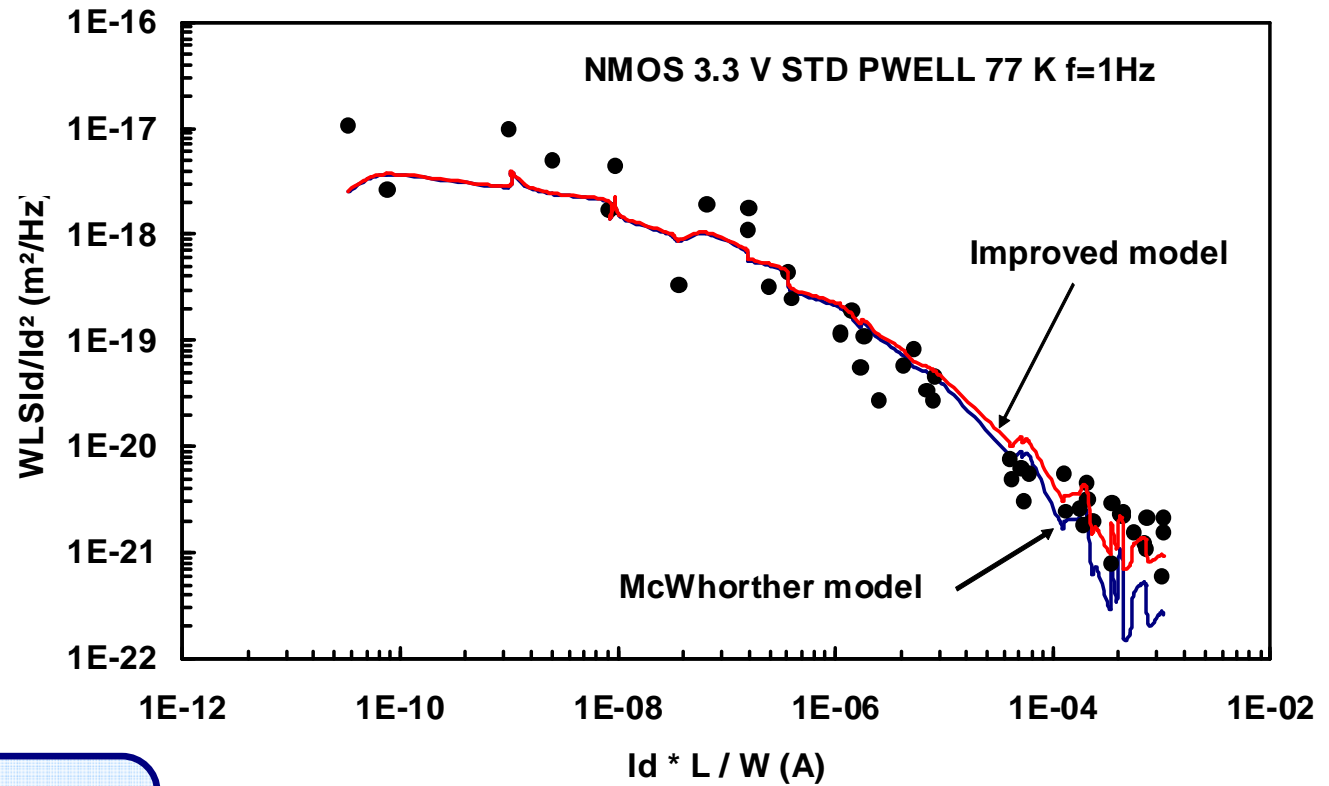
# Noise measurements at 77 K



**PMOSFET 3.3 V**  
**77 K, f=1 Hz**

- Improved McWhorter's model still applicable at low temperature
- Lower  $\alpha$  Coulomb scattering coefficient ( $5 \times 10^3$  @ 77 K)
- Same result for NMOSFET, but different  $\alpha$  coefficient

# Noise measurements at 77 K



**NMOSFET 3.3 V**  
**77 K, f=1 Hz**

# LF noise modeling in EKV2.6 and EKV3

**EKV2.6**

$$S_{Id} = \frac{KF G_m^2}{C_{ox} W_{eff} L_{eff} f^{AF}}$$

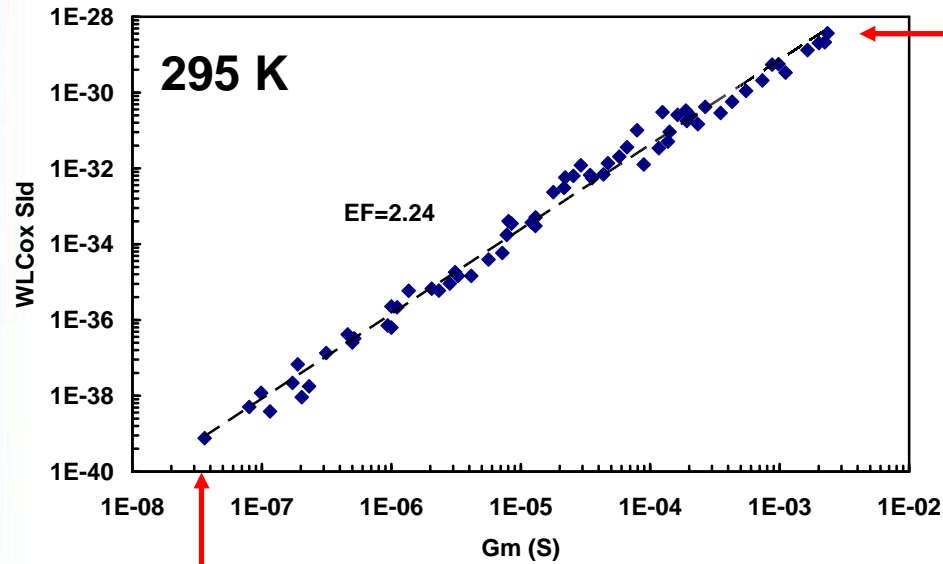
**EKV3**

$$S_{Id} = \frac{KF G_m^{EF}}{C_{ox} W_{eff} L_{eff} f^{AF}}$$

↓  
Three parameters: KF, AF, EF ≠ 2

**Simple but efficient model ?**

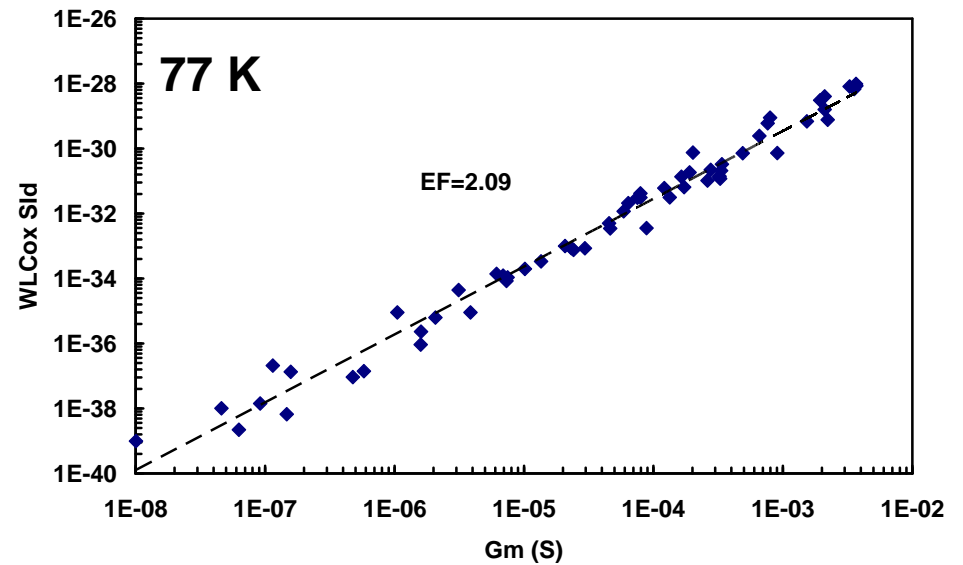
# Experimental results: NMOSFET



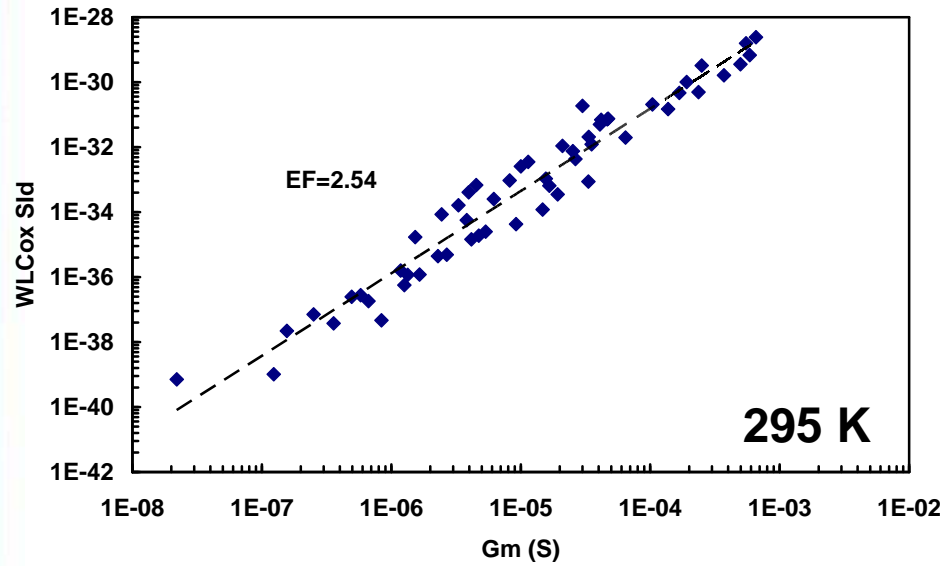
**strong inversion**  
 $I_d \sim 1-10 \text{ mA}$

**NMOSFET STD 3.3 V P-WELL**  
 $T_{ox}=6.5 \text{ nm } f=1 \text{ Hz}$

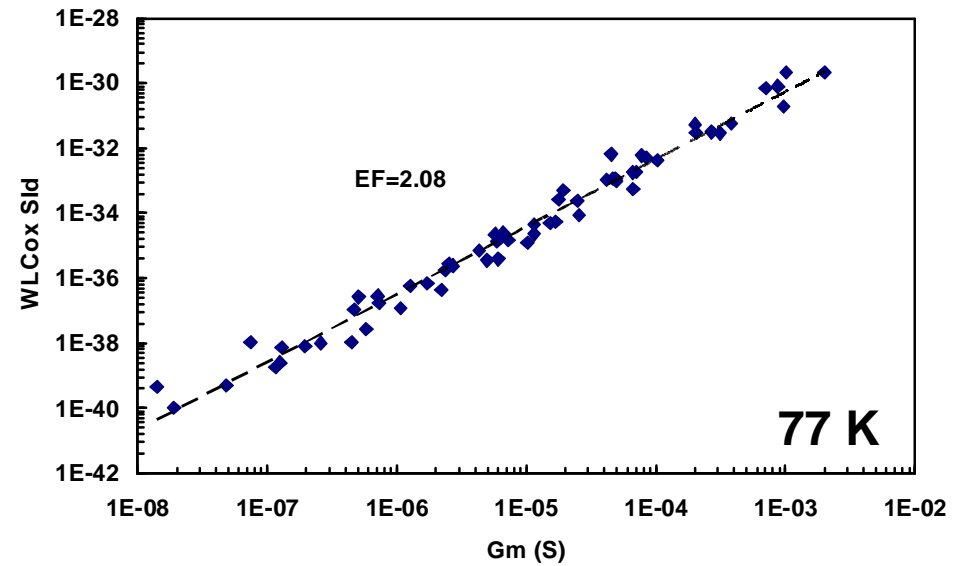
**weak inversion**  
 $I_d \sim 0.1-1 \text{ nA}$



# Experimental results: PMOSFET



PMOSFET STD 3.3 V N-WELL  
Tox=6.5 nm f=1 Hz



## Conclusion

- **Presentation of some LFN measurements at low temperature in a 0.18  $\mu\text{m}$  mixed-mode CMOS technology**
- **The improved McWhorter's model is applicable for NMOSFET and PMOSFET at RT and 77 K**
- **The Coulomb scattering coefficient is temperature dependent, it is lower at 77 K than at RT**
- **The LF noise model implemented in EKV3 allows precise modeling at RT and also at low temperature in a very wide range, from weak to strong inversion**
- **Measured EF parameter values are  $> 2$  and are device- and technology-dependent**
- **Simple but efficient model for analog/digital IC design**