



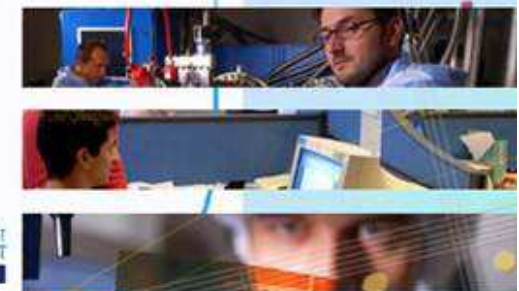
## Analytical modelling and performance analysis of Double-Gate MOSFET-based circuit including ballistic/quasi-ballistic effects

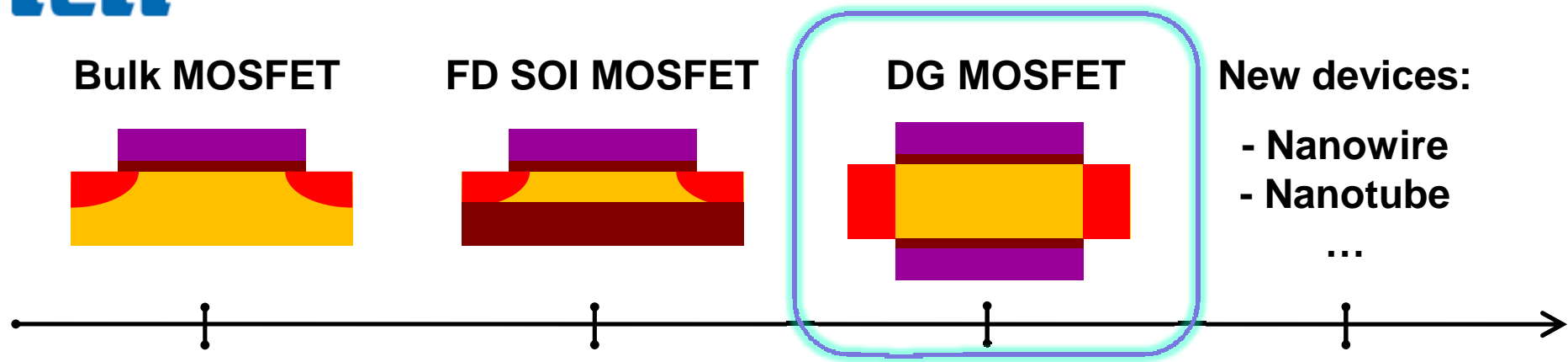
S. Martinie, D. Munteanu,  
G. Le Carval, M.-A. Jaud and J.L. Autran



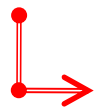
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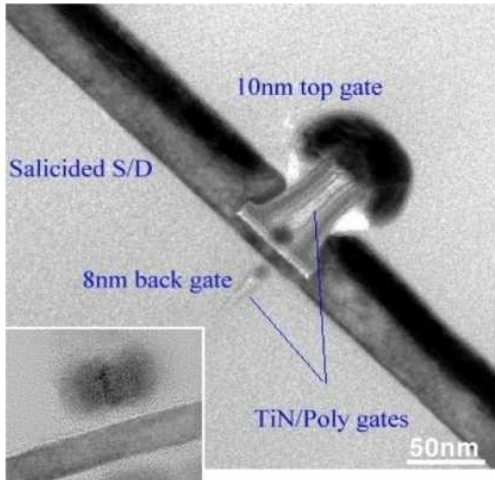




- Excellent electrostatic control •→ Better  $I_{on}/I_{off}$  ratio
- Undoped film (no more doping fluctuation) •→  $\mu \nearrow$
- The reduction of channel length •→ New transport properties



To take advantage of this new devices, designers need a model, particularly a compact model to design future circuits.



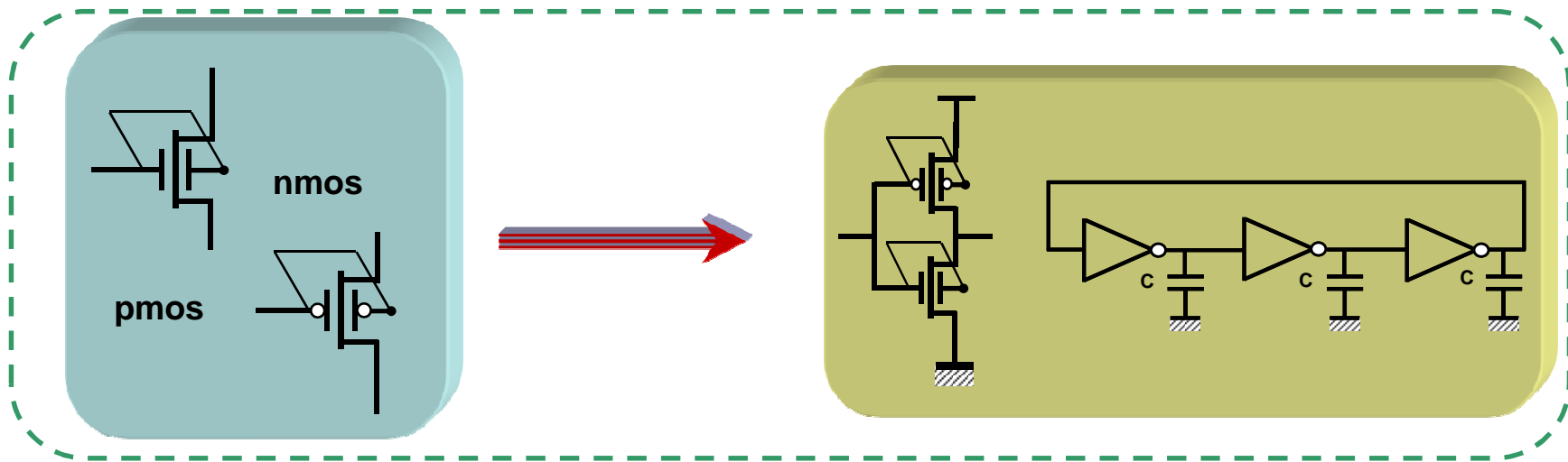
M. Vinet *et al* CEA-LETI 2007

Downscaling of MOSFET to nanometric dimensions



Emergence of physical phenomena such as ballistic transport

**Evaluation of the ballistic transport on circuit performances**  
**Verilog-A environment**



- I) Introduction**
- II) Physics of ballistic transport**
- III) Our compact model**
- IV) Device and circuit results**
- V) Conclusion**

## I) Introduction

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## V) Conclusion

## II.1) Schematic view of electronic transport

Diffusive transport



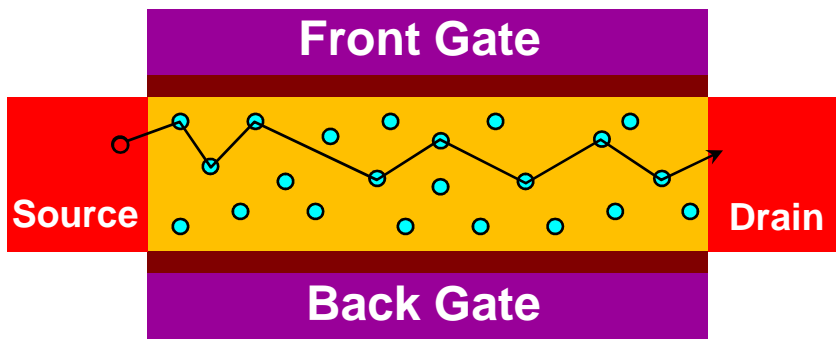
Quasi ballistic transport



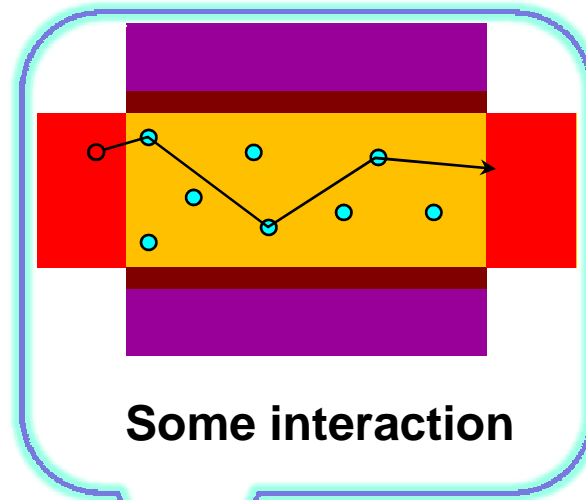
Ballistic transport



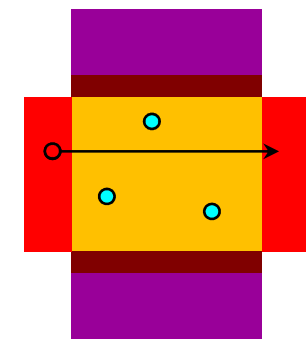
Downscaling to nanometric dimensions



A lot of interaction



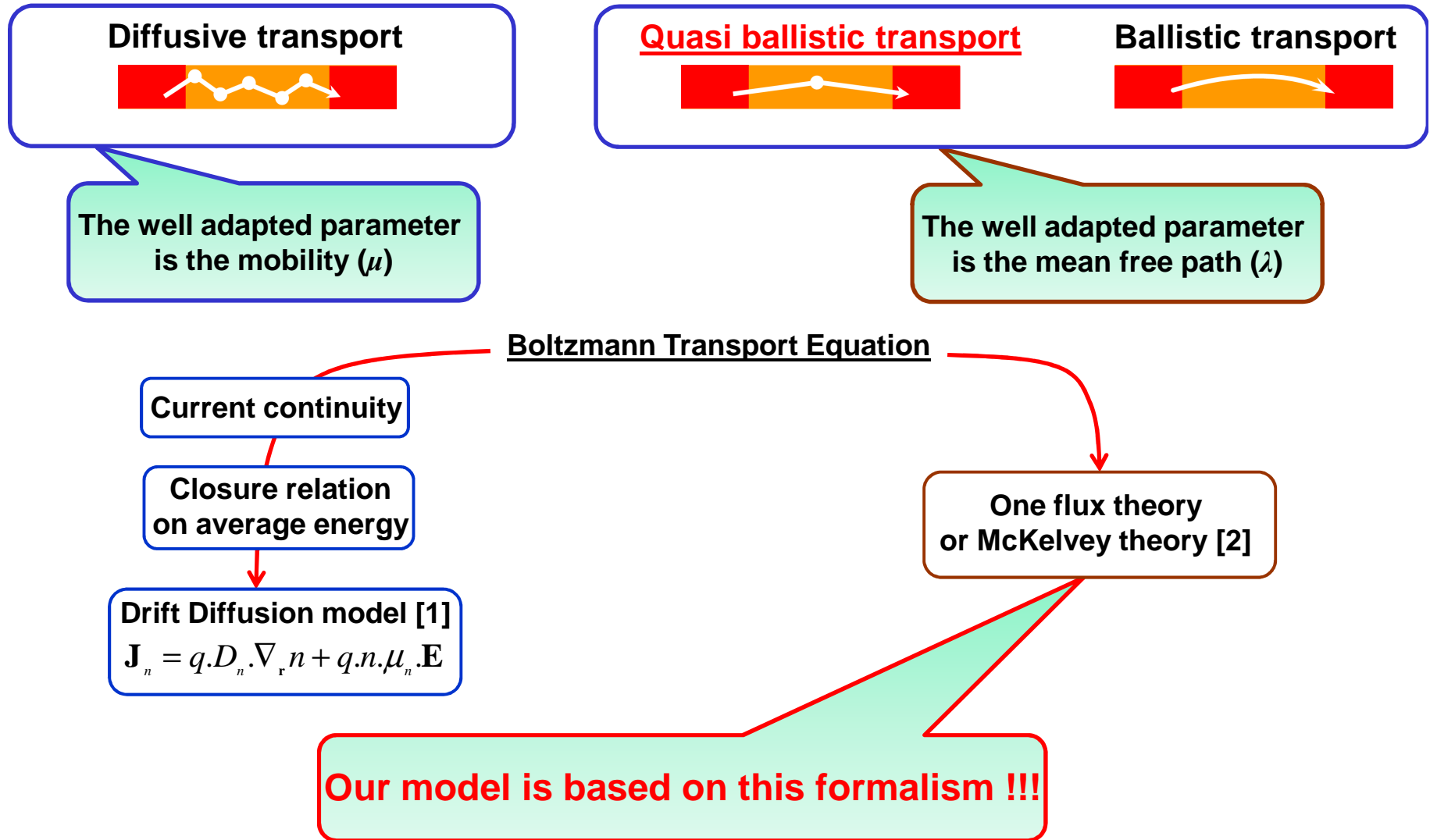
Some interaction



No interaction

**The main difficulty is to implement an unified compact model which links the diffusive to the ballistic transport.**

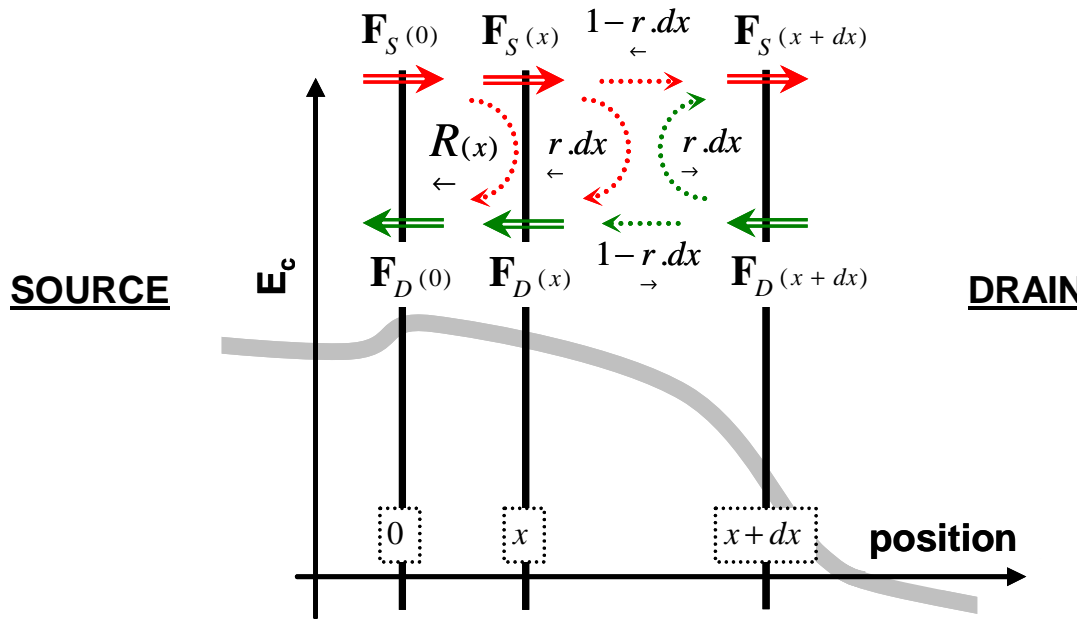
## II.1) Schematic view of electronic transport



[1] T. Grasser *et al* TED Feb 2003

[2] J. P. McKelvey *et al* Phys. Rev 1961

### II.3) Physical assumption on backscattering coefficient



**Physical assumption [1-2] :**

- Non degenerate semiconductor
- Constant electric field
- No generation or recombination

- $\mathbf{F}_D(x)$  = Drain flux
- $\mathbf{F}_S(x)$  = Source flux
- $R(x)$  = Backscattering coefficient
- $r$  = scattering probabilities
- $\mathbf{F}_D(x)/\mathbf{F}_S(x) = R(x)$

**For constant electric field :**

$$R_{\leftarrow} = \frac{r_{\leftarrow}}{\sqrt{r_{\rightarrow}^2 - r_{\rightarrow} \cdot r_{\leftarrow}} \cdot \coth\left(x \cdot \sqrt{r_{\rightarrow}^2 - r_{\rightarrow} \cdot r_{\leftarrow}}\right) + r_{\rightarrow}}$$

with :  $r_{\rightarrow} = (r_{\rightarrow} + r_{\leftarrow}) / 2$

With :  $r_{\rightarrow} = \lambda^{-1} - \frac{q \cdot E}{k_B \cdot T_L}$  and  $r_{\leftarrow} = \lambda^{-1}$

$$R_{\leftarrow} = \frac{\lambda^{-1}}{\left(L \cdot \frac{kT}{q \cdot V_{DS}}\right)^{-1} \cdot \left(\frac{1}{1 - e^{-\frac{q \cdot V_{DS}}{k \cdot T}}}\right) + \lambda^{-1}}$$

$$R_{\leftarrow} = \frac{L_{kT}}{L_{kT} + \lambda}$$

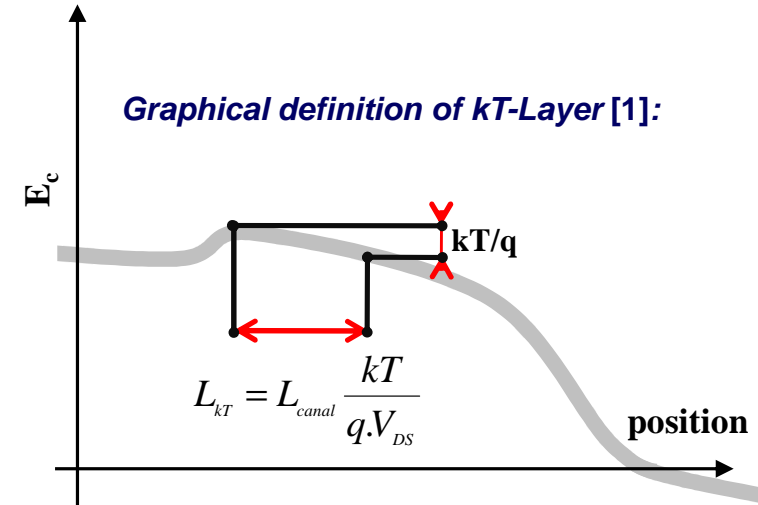
M. Lundstrom  
EDL Jul. 1997

## II.4) Current model

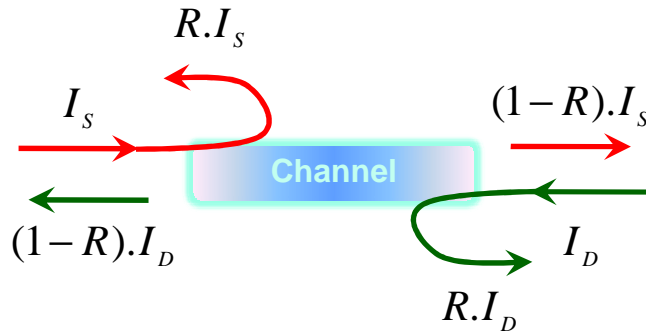
### The Backscattering coefficient [1]:

$$R = \frac{L_{kT}}{L_{kT} + \lambda}$$

$L_{kT}$ : kT-Layer: the critical distance over which scattering events modify the current  
 $\lambda$ : mean free path: effect of scattering process



### We obtain the current versus potential characteristics in presence of scattering processes:



$$I = W \cdot C_{ox} \cdot (V_{GS} - V_T) \cdot v_{th} \cdot \left( \frac{1-R}{1+R} \right) \left( \frac{1 - e^{-qV_{DS}/k.T}}{1 - \left( \frac{1-R}{1+R} \right) e^{-qV_{DS}/k.T}} \right)$$

with :  $v_{th} = \sqrt{\frac{2 \cdot k \cdot T}{\pi \cdot m^*}}$  is the thermal velocity

$W$  : gate width /  $C_{ox}$  : gate oxide capacitance /  $V_{GS}$  : gate to source voltage /  $V_{DS}$  the drain to source voltage  
 $V_T$  : threshold voltage /  $k$  the Boltzmann constant /  $q$  : electron charge /  $T$  the lattice temperature.

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**III) Our compact model**

**IV) Device and circuit results**

**V) Conclusion**

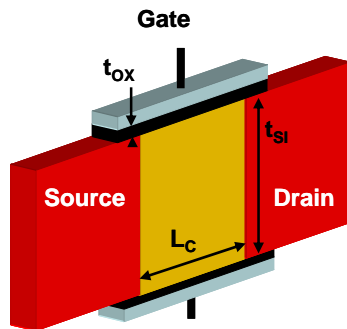
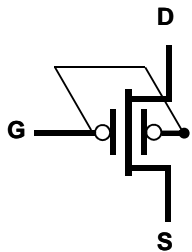
### III.1) Verilog-A model

$$V_T = V_{th} - \Delta V_T$$

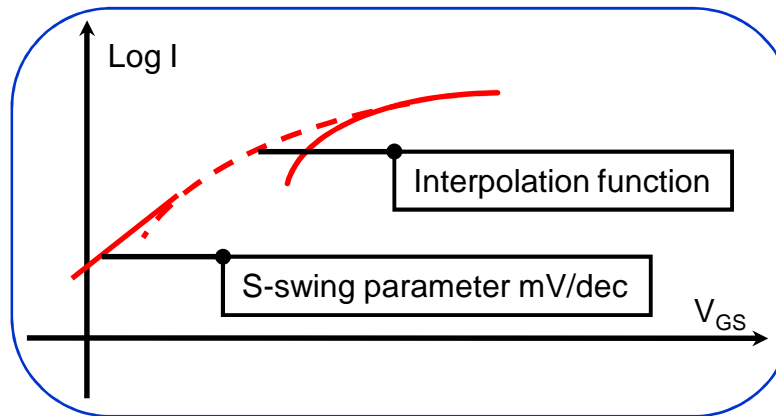
$$\frac{1-R}{1+R} = \frac{dfp}{2.L_{KT} + dfp} \quad \text{and} \quad dfp(\epsilon_{bal}) = v_{bal} \cdot \tau_{tot}$$

- Ionized impurity, optical and acoustic phonon are taking to account here.

Symmetrical Double gate



$$I = 2 \cdot W \cdot f(s) \cdot C_{ox} \cdot (V_{GS} - V_T) \cdot v_{th} \cdot \left( \frac{1-R}{1+R} \right) \cdot \left( \frac{1 - e^{-qV_{DS}/k.T}}{1 - \left( \frac{1-R}{1+R} \right) e^{-qV_{DS}/k.T}} \right)$$



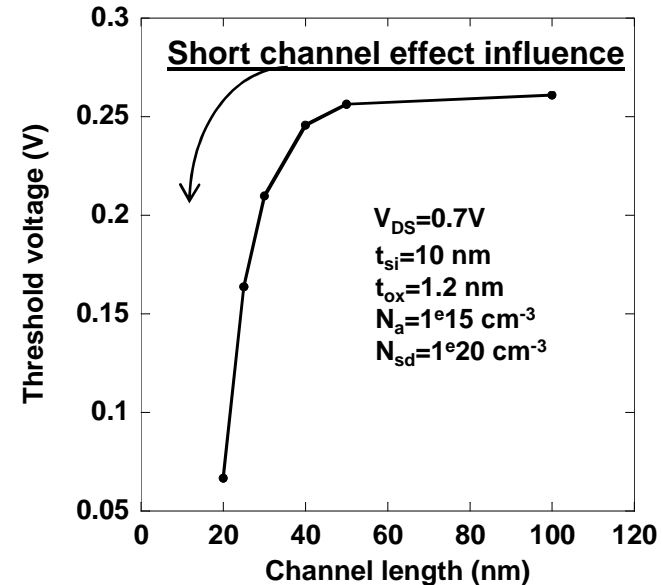
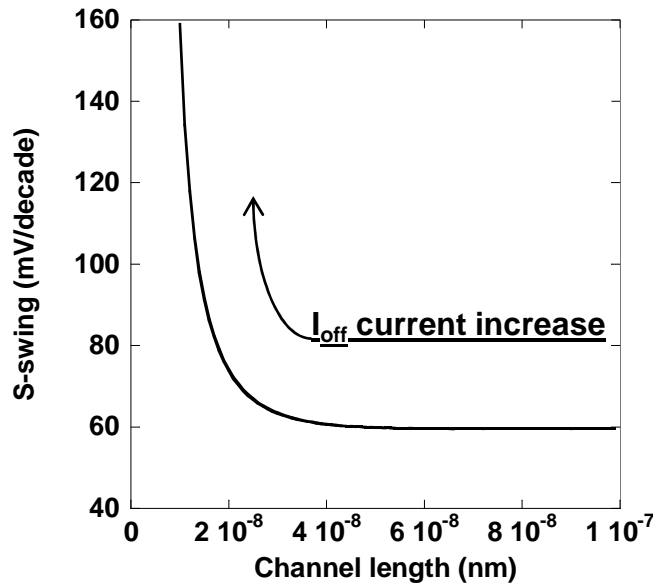
### III.2) Short channel effects

Threshold voltage use the Suzuki's model and our interpolation function [1]:

$$I = 2.W \cdot f(S) \cdot C_{ox} \cdot (V_{GS} - V_T) \cdot v_{th} \cdot \left( \frac{1-R}{1+R} \right) \left( \frac{1 - e^{-qV_{DS}/k.T}}{1 - \left( \frac{1-R}{1+R} \right) e^{-qV_{DS}/k.T}} \right)$$

interpolation function
S\_swing parameter

$V_T = V_{th} - \Delta V_T$



**Finally our model describes all short channel effects by using the analytical Suzuki's approach.**

### III.3) Dynamical mean free path

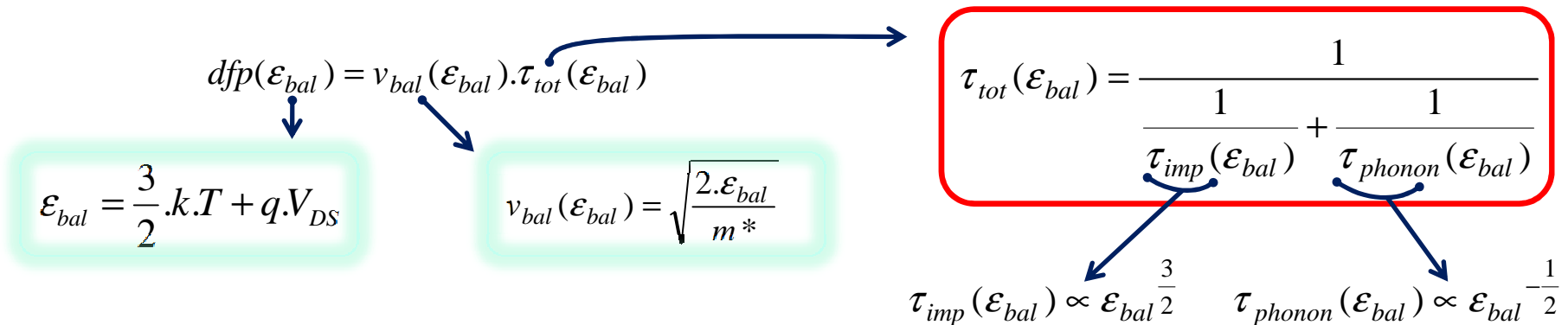
As described before the backscattering coefficient was:

$$R = \frac{L_{kT}}{L_{kT} + \lambda}$$

We introduce a new characteristic length (*dfp*)

$$R = \frac{L_{kT}}{L_{kT} + dfp}$$

The definition of this dynamical mean free path (*dfp*) [1] is:



**The dynamical mean free path is a local free path of ballistic carriers. It represents the average distance to be crossed before the next scattering event.**

**I) Introduction**

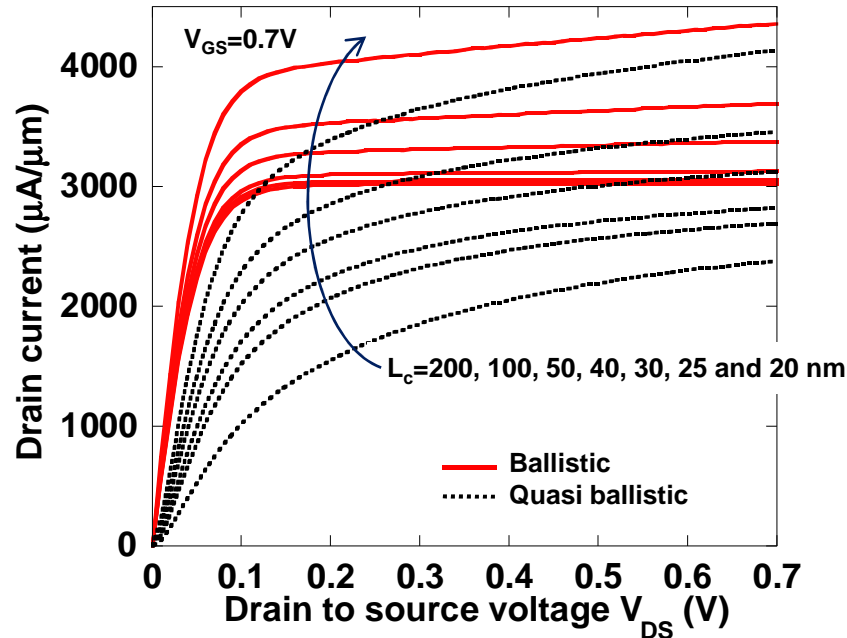
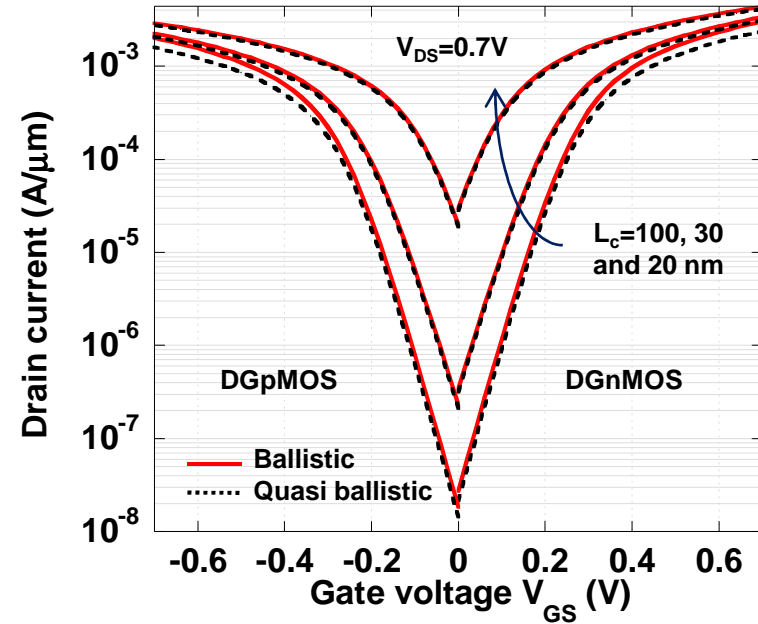
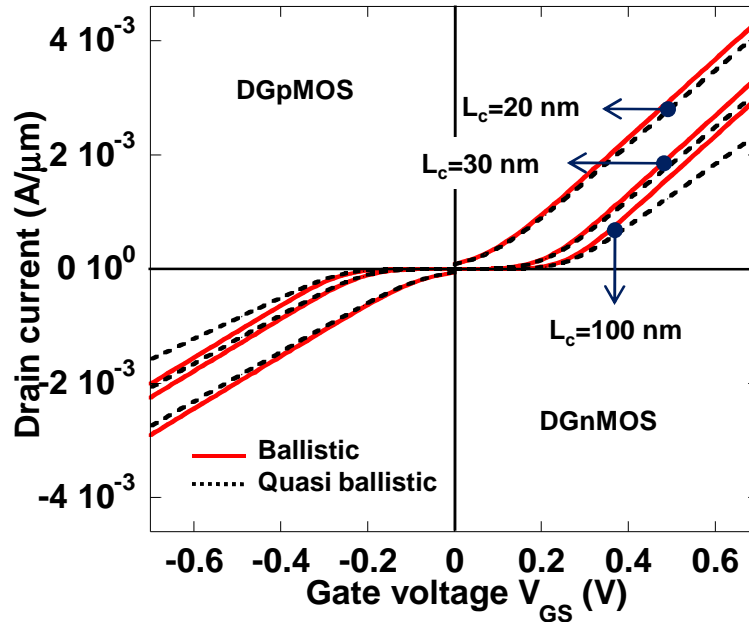
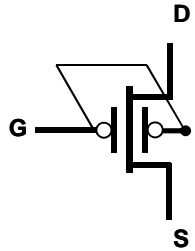
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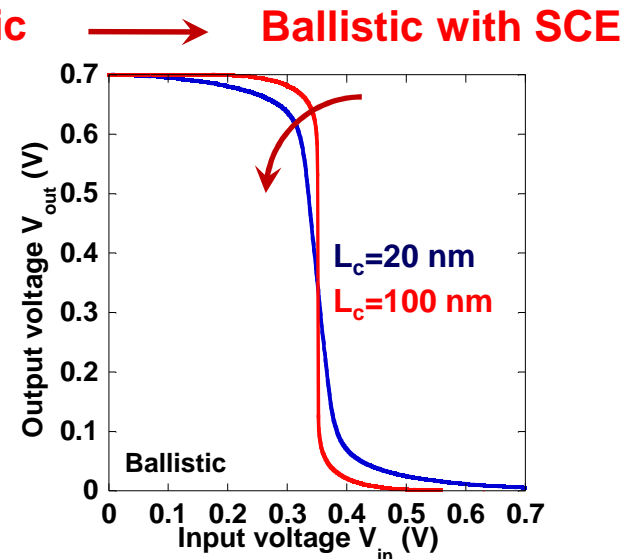
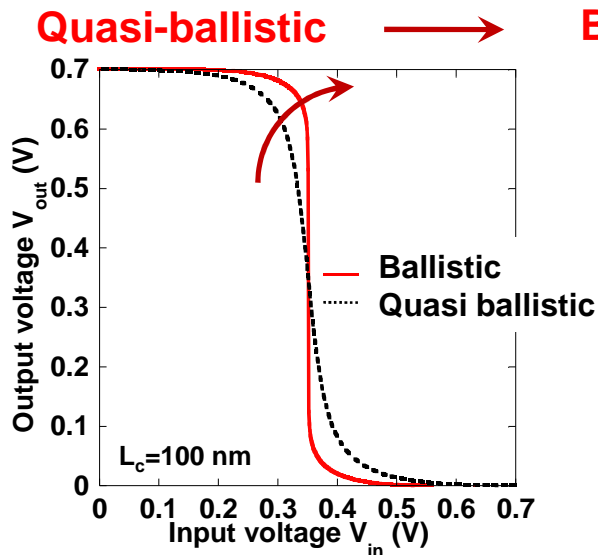
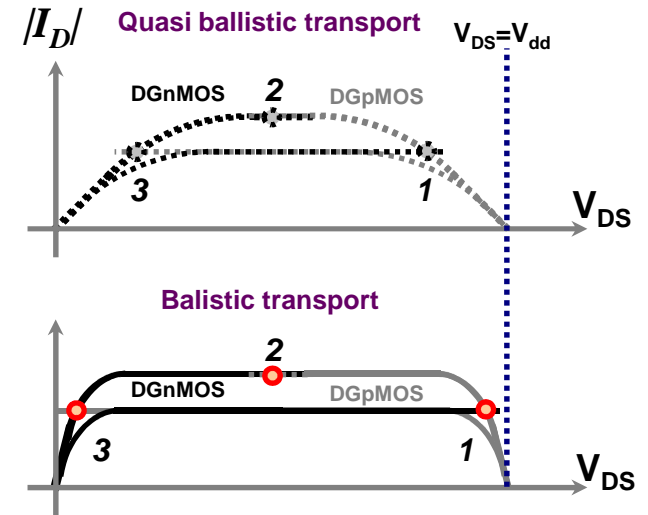
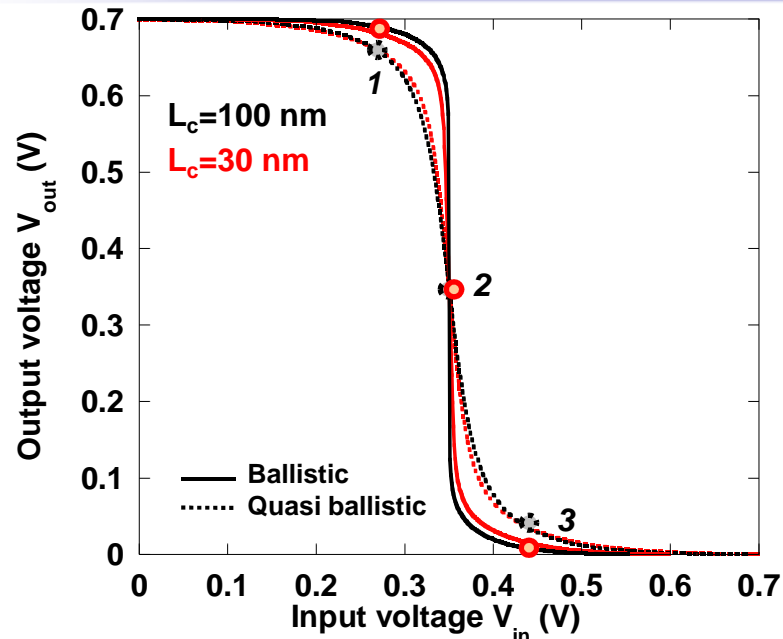
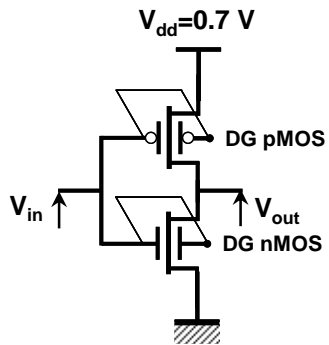
# IV.1) Device simulation DGnMOS and DGpMOS



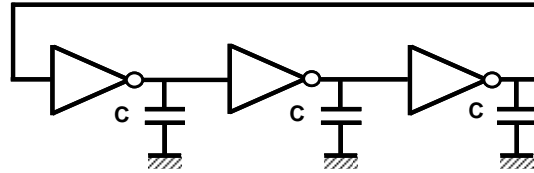
- The drain current is independent of the channel length in ballistic case, except for the smallest lengths for which short channel effects are exacerbated.

- In contrast to the ballistic case, the quasi-ballistic transport has the same behavior compared to diffusive transport.

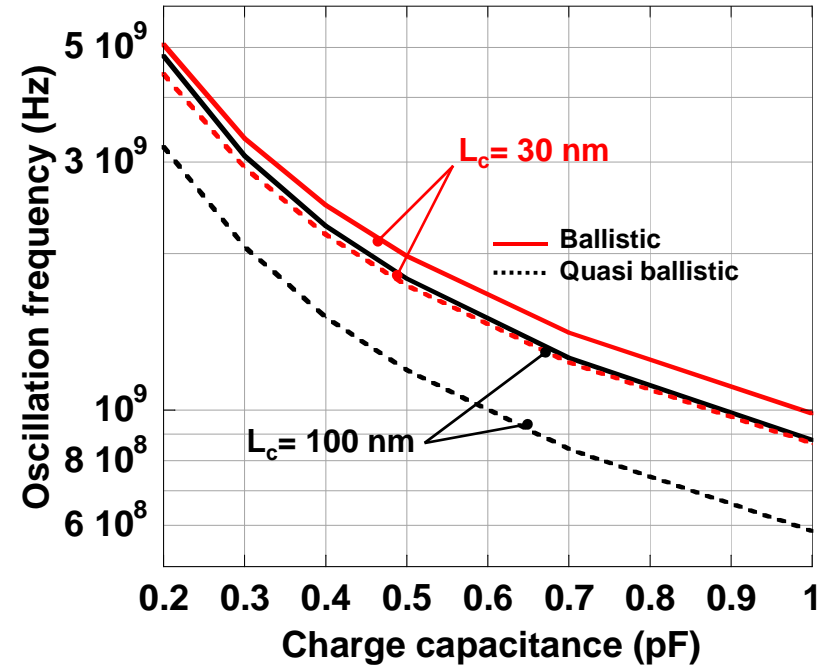
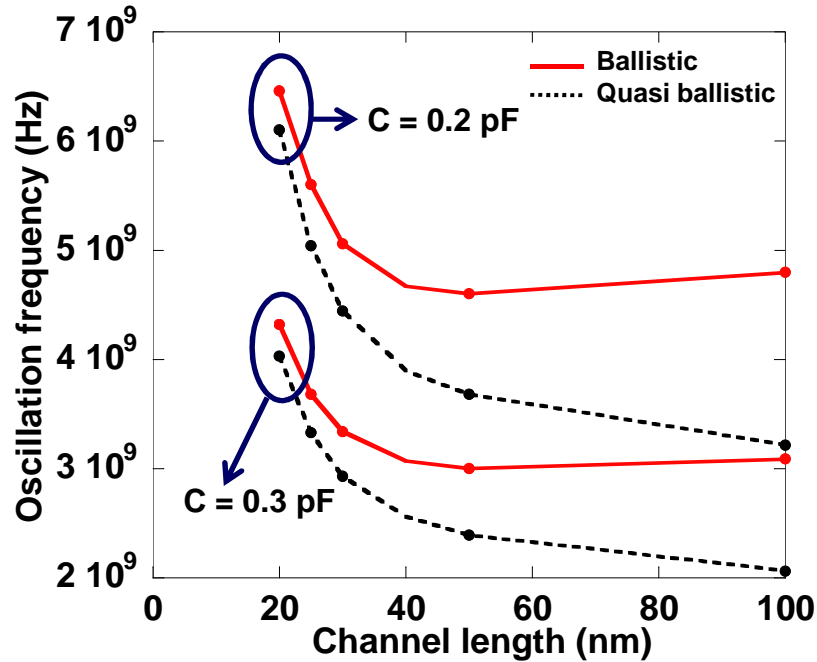
## IV.2) CMOS inverter



### IV.3) Ring oscillator



$$f^{-1} = 2.N.t_d = C \cdot \int_{0.9V_{dd}}^{0.1V_{dd}} \frac{1}{I} dV_{DS}$$



**-Oscillation frequency is strongly impacted by short channel effects and the type of transport (Ballistic/Quasi-Ballistic)**

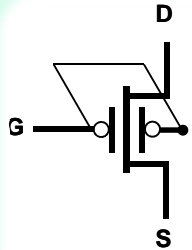
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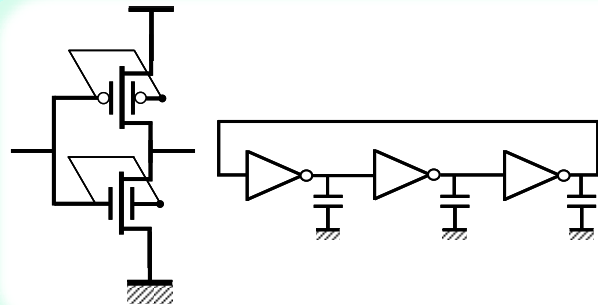
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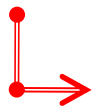
**V) Conclusion**



➔ A unified compact model for DG MOS taking into account Ballistic / Quasi-Ballistic transport and short channel effect has been developed and implemented in Verilog-A environment to obtain a complete description of current characteristics (sub 32 nm node)



➔ Our simulation results proved that the Ballistic transport improves the static and transient performances of circuit elements.



**This work also demonstrates the feasibility of a simulation study of Ballistic / Quasi-Ballistic transport at circuit level and highlights the direct relation between the type of transport and circuit performances**

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THANK YOU FOR ATTENTION

